IE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814 Examiner: MAI, ANH D

In Re PATENT APPLICATION Of:

Applicant

: Hiroki NAKAMURA

Serial No.

: 09/625,178

Filed

: July 25, 2000

For

: SEMICONDUCTOR DEVICE HAVING

WIRING PATTERNS AND DUMMY

PATTERNS COVERED WITH

INSULATING LAYER

Attorney Ref.: F98ED0762

June 29, 2005

AMENDMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Examiner's Office Action mailed May 11, 2005, please amend the above-identified application as follows: